



## Spring 2019 UEFI Forum Plugfest Session Abstracts and Schedule

Company	Session Title & Abstract	Presenter	Date/Time
UEFI Forum	Welcome: State of UEFI	-Dong Wei, UEFI Forum Vice President	Tue 4/9 9:00 – 9:30 a.m.
Arm, Intel Corporation, Canonical Group Ltd.	<p><b>Title:</b> UEFI Self Certification Tests (UEFI-SCT) and Firmware Test Suite (FWTS)</p> <p><b>Abstract:</b> In the past, UEFI-SCT existed in a private GitHub repository and source code access was restricted. UEFI-SCT binaries/tools could only be obtained by adopting the UEFI Adopter Membership Agreement. This session discusses behind the scenes initiatives that took place in the last year to make UEFI-SCT open source and encourages contributions from open source enthusiasts, supporters, UEFI forum partners etc. It also presents new contribution guidelines on how to contribute to UEFI-SCT and requests contributions with a humble thank you.</p> <p>Firmware Test Suite (FWTS) is the recommended ACPI SCT. It is licensed by GPL which ensures it is free to use, modify and redistribute. Contributing to FWTS is easy, and it is always welcomed and appreciated. FWTS-LIVE also receives a major update - it is now based on latest Ubuntu 18.04. Finally, FWTS-LIVE's build scripts are open source for everybody who wants customization.</p>	-Supreeth Venkatesh, Arm  -Harry Hsiung, Intel Corporation  -Alex Hung, Canonical Group Ltd.	Tue 4/9 9:30 – 10:00 a.m.
Arm	<p><b>Title:</b> UEFI Updates and Open Source Firmware evolutions on Arm</p> <p><b>Abstract:</b> The session will present the latest updates on UEFI Requirements for Arm and the latest news on Arm specifications. It will then discuss the latest evolutions in Open Source Firmware to handle the increasingly complex challenges arose in the Secure world software space by many different UEFI-based ecosystems, including the new Edge and Networking market segments.</p>	- Matteo Carlini - Dong Wei	Tue 4/9 10:00 – 10:30 a.m.
Phoenix	<p><b>Title:</b> Risks to UEFI firmware due to growing attack surfaces</p> <p><b>Abstract:</b> The addition of networking stacks and services, the necessity for “automatic” firmware updates and other feature enhancements are presenting new attack surfaces in UEFI based firmware for bad actors to probe and for defenders to protect. We will provide some examples of dangerous and poorly implemented features and make proposals for actions the UEFI community should consider.</p>	- Dick Wilkins	Tue 4/9 10:30 – 11:00 a.m.

Spring 2019 UEFI Plugfest – Session Details and Schedule – Subject to Change

<p>Intel Corporation Session</p>	<p><b>Title:</b> From Runtime to Compile Time: Improving ASL Through Enhanced Namespace Resolution</p> <p><b>Abstract:</b> The ACPI Source Language (ASL) was designed for firmware to describe platform-specific details to operating systems. This language is compiled to ACPI Machine Language (AML) bytecode which is interpreted in the OS kernel-space. Like other languages executed on a bytecode interpreter, AML has the possibility of incurring runtime errors due to programming mistakes.</p> <p>This session describes a class of runtime errors that are commonly found on deployed products, and a new Intel ASL compiler feature designed to eliminate these issues.</p>	<p>- Erik Schmauss</p>	<p>Tue 4/9 11:00—11:30 a.m.</p>
<p>Microsoft</p>	<p><b>Title:</b> Microsoft UEFI Updates for 2019</p> <p><b>Abstract:</b></p>	<p>- Jeremiah Cox</p>	<p>Tue 4/9 11:30 a.m.— 12:00 p.m.</p>
<p>Intel Corporation</p>	<p><b>Title:</b> Hardening Firmware Components with Host-based Analysis</p> <p><b>Abstract:</b> Platform firmware is a critical element for root-of-trust. Attackers are increasingly targeting firmware to deploy persistent attacks, often based on issues not detected through traditional platform validation and integration testing. Software validation methods like fuzz testing, static analysis, and fault injection are typically difficult to apply to firmware. Integration testing, the most common method of firmware validation, makes it difficult to isolate errors and identify faulty modules.</p> <p>This session describes host-based analysis, a new method developed by Intel to isolate firmware components and check for common software issues prior to integration with platform firmware. Host-based Firmware Analyzer (HBFA) is an open source project designed to harden firmware modules using best-in-class software validation tools.</p> <p>HBFA will be contributed to TianoCore, the open source community for UEFI development. This is a tool for firmware component analysis with a focus on fuzzing and symbolic testing of firmware components. Host-based methods isolate firmware components in the developer’s OS environment and leverages existing open source analysis tools (ex: AFL, Peach, KLEE).</p> <p>This session provides an overview of tool and how it is used to improve efficiency of firmware security unit test cases.</p>	<p>-Brian Richardson</p>	<p>Tue 4/9 12:30—1:00 p.m.</p>

Spring 2019 UEFI Plugfest – Session Details and Schedule – Subject to Change

NXP	SESSION CANCELED DUE TO TRAVEL ISSUES		Wed 4/10 11:00—11:30 a.m.
Intel Corporation	<p><b>Title:</b> Improving UEFI Network Stack Performance</p> <p><b>Abstract:</b> Network booting is an important pre-OS feature. Technologies like PXE and iSCSI Network Boot, defined almost two decades ago, are still being widely used on legacy BIOS and UEFI server platforms. However, pre-OS networking typically falls short of OS-based network performance, mostly due to background events.</p> <p>This session is a proposal for improving UEFI network stack performance, based on a proof-of-concept for performance-oriented design utilizing multiprocessing mechanisms (UEFI MP Services Protocol) and a lightweight TCP/IP stack (lwIP). The presentation includes results achieved in comparison to existing network boot implementations.</p>	- Maciej Rabeda -Vincent Zimmer	Wed 4/10 11:30 a.m.— 12:00 p.m.
Intel Corporation	<p><b>Title:</b> Case Study: Removing SMM from Intel Platforms</p> <p><b>Abstract:</b> The broadcast System Management Mode (SMM) model has been used for many years to manage priority system events but has a number of disadvantages. Overuse of System Management Interrupts (SMI) results in performance degradation, increases latency with higher core counts, and introduces potential race conditions. SMM is also difficult to debug and has access to system resources outside of the OS environment, which makes it target for firmware exploits.</p> <p>This session expands on Intel’s initiative to reduce SMM footprint and provide alternatives for handling runtime platform events. Intel described SMI reduction methods based on Protected Runtime Mechanism (PRM), UEFI Capsule, and the Baseboard Management Controller (BMC) at the 2018 OCP Regional Summit. The presentation features a case study and demonstration using Intel® Xeon® Scalable Processors with EDK II firmware.</p>	- Sarathy Jayakumar	Wed 4/10 12:30—1:00 p.m.
NXP	CANCELED DUE TO TRAVEL ISSUES - BREAK		Wed 4/10 1:00—1:30 p.m.
Intel Corporation	<p><b>Title:</b> Role Modeling Open Source Best Practices in Firmware</p> <p><b>Abstract:</b> Since 2004, the TianoCore community has been a model for open source firmware implementations of UEFI. Today, we continue to grow our community by improving our infrastructure and aligning with current open source best practices. As our project matures, we</p>	-Mark Doran -Stephano Cetola	Wed 4/10 1:30—2:00 p.m.

Spring 2019 UEFI Plugfest – Session Details and Schedule – Subject to Change

	<p>have worked to improve our licensing to be less restrictive, allowing more contributions from more organizations.</p> <p>This session presents changes being made to TianoCore based on community feedback. These efforts come with a unique set of challenges, as partners demand open source further down the stack. Customer needs often do not line up directly with community expectations, so finding a common ground can require creative problem solving. We have leveraged feedback through monthly community meetings and opened our design process up to involve the community early in the development cycle. The innovation and perspective gained is well worth the effort to show how open is the new normal, even in firmware.</p>		
American Megatrends Inc.	<p><b>Title:</b> Using Capsules for Firmware Configuration Update</p> <p><b>Abstract:</b> Capsules have been used by UEFI for updating of device firmware for several years. UEFI 2.8 has introduced a new feature where the firmware exposes HII configuration information to the operating system. The operating system or additional tools can in turn provide a capsule back to the firmware for updating of HII configuration settings. This presentation will be an overview of this new feature in UEFI 2.8 and explores real world use cases.</p>	- Zachary Bobroff	Wed 4/10 2:00—2:30 p.m.
Flex Institute of Technology	<p><b>Title:</b> UEFI topics for the manufacturing efficiency</p> <p><b>Abstract:</b> After passing the design phase of a new equipment other challenges arise, when thousands of devices start to be manufactured, with the target to deliver to the users' equipment that work as expected. At this presentation, the manufacturing point of view will be presented to the UEFI community, presenting situations related to the BIOS, showing real scenarios that could be improved by the BIOS teams and ideas that would make the manufacturing process more efficient.</p>	- Rafael Machado	Thurs 4/11 12:30—1:00 p.m.
Linaro	<p><b>Title:</b> How Writing Portable UEFI Drivers Improves Reliability (and Helps Me)</p> <p><b>Abstract:</b> UEFI provides all the interfaces needed to write software portable between different architectures. However, many current executables have only been validated on a single platform. Through a joint effort between SuSe and Linaro, we emulated X64 option ROMs on ARM systems which let us find some common mistakes you need to avoid when writing drivers to run on ARM... or work through more than accident elsewhere.</p> <p>This talk gives a summary of common mistakes, how to avoid them, and other things that would make my life easier.</p>	- Ard Biesheuvel - Leif Lindholm	Thurs 4/11 1:00—1:30 p.m.

Spring 2019 UEFI Plugfest – Session Details and Schedule – Subject to Change

<p>HPE</p>	<p><b>Title:</b> Redfish Implementation for UEFI</p> <p><b>Abstract:</b> The DMTF Redfish specification defines a management standard for scalable enterprise systems with one the goals being to improve interoperability. However, the BIOS attribute registry and BIOS schemas were not standardized by the DMTF. Instead, it was left for the support of BIOS settings to be implementation specific. Hence, all BIOS attributes across the various IBV implementations are unique and heterogenous. HPE would like to present implementation guidance for UEFI vendors so that the construction of the Redfish BIOS resource is interoperable across the datacenter. The guidance requires updates to the DMTF schemas and UEFI specification to promote a common translation of HII formsets to Redfish attribute registries.</p>	<p>-Jason Spottswood</p>	<p>Thurs 4/11 1:30—2:00 p.m.</p>
<p>Intel Corporation and Lenovo Corporation</p>	<p><b>Title:</b> Redfish Host Interface: UEFI and OS implications</p> <p><b>Abstract:</b> Learn about the DMTF Redfish Host Interface and its relationship to UEFI, BMC firmware, and Operating Systems. See a demo of the latest OS support for this industry standard interface for in-band access to the Redfish REST API. Discuss use-cases for system management, as well as challenges and next steps in ecosystem enablement.</p>	<p>- Samer El Haj Mahmoud, Lenovo Corporation -John Leung, Intel Corporation -Mike Rothman, Intel Corporation</p>	<p>Thurs 4/11 2:00—2:30 p.m.</p>